

**Amendments to the Claims:**

A clean version of the entire set of pending claims (including amendments to the claims, if any) is submitted herewith per 37 CFR 1.121(c)(3). This listing of claims will replace all prior versions, and listings, of claims in the application.

**Listing of Claims:**

1. (Currently amended) A matrix display device comprising an array of addressable pixels, each pixel having a display element and a control circuit for controlling the operation of the display element, the control circuit comprising:

a charge storage capacitor and a photosensitive device coupled to the storage capacitor for regulating charge stored on the storage capacitor in accordance with light ~~falling on~~ received by the photosensitive device[[,]];:

a driving element for driving the display element, a control terminal of the driving element being connected to said storage capacitor[[,]];:

an addressing element for applying a data signal to the driving element[[,]]; and  
means for independent voltage control of a control terminal of the photosensitive device.

2. (Currently amended) A The matrix display device according to claim 1, wherein the independent voltage control means comprise a selection line ~~being~~ connected to ~~the~~ a gate terminal of the photosensitive device.

3. (Currently amended) A The matrix display device according to claim 1, wherein the photosensitive device comprises a thin film transistor of the same conductivity type as a conductivity type of the driving element and the addressing element.

4. (Currently amended) A The matrix display device according to claim 1, wherein the

display element comprises an Organic Light Emitting Diode.

5. (Currently amended) A The matrix display device according to claim 2, wherein the selection line is individually addressable for each selection line of the display.

6. (Currently amended) A The matrix display device according to claim 2, wherein the selection line is formed by a single common terminal.

7. (Currently amended) A display apparatus, comprising:  
a matrix display device as claimed in claim 1,  
a data driver circuit for applying said data signal to a data terminal of the addressing switch element; and  
a selection driver circuit for applying a selection signal to said ~~selection line~~ means for independent voltage control.

8. (Currently amended) A The display apparatus according to claim 7, wherein said independent voltage control means comprise duty cycle control means.

9. (New) A control circuit for controlling a display element of a pixel in a pixel array, the control circuit comprising:  
a drive transistor for driving the display element;  
an address transistor for sending a data signal to the drive transistor, a gate of the address transistor being connected to a first selection line;  
a capacitor connected to a gate of the drive transistor; and  
a photosensitive transistor coupled to the capacitor for regulating a charge stored on the capacitor in accordance with light emitted from the display element, a gate of the photosensitive transistor being connected to a second selection line for providing independent voltage control.

10. (New) The control circuit according to claim 9, wherein each of the drive transistor, the address transistor and the photosensitive transistor comprises a thin film transistor.

11. (New) The control circuit according to claim 9, wherein each of the drive transistor, the address transistor and the photosensitive transistor comprises a p-type transistor.

12. (New) The control circuit according to claim 9, wherein each of the drive transistor, the address transistor and the photosensitive transistor comprises an n-type transistor.

13. (New) The control circuit according to claim 9, wherein the first selection line comprises a first row line.

14. (New) The control circuit according to claim 13, wherein the second selection line comprises a second row line set to a separate voltage.

15. (New) The control circuit according to claim 9, wherein the second selection line is individually addressable.

16. (New) The control circuit according to claim 9, wherein the second selection line is formed by a single common terminal.

17. (New) A control circuit for controlling a display element of a pixel in a pixel array, the control circuit comprising:

a first transistor for driving the display element, the first transistor being connected in series between a common current line and a common voltage supply line of the pixel array;

a second transistor for applying a data signal to the drive transistor, a gate of the second transistor being connected to a row selection line;

a capacitor connected to a gate of the first transistor; and

a third transistor coupled to the capacitor to regulate a charge stored on the capacitor in accordance with light received by the third transistor from the display element, a gate of the third transistor being connected to a separate source voltage.